

FIG. 1

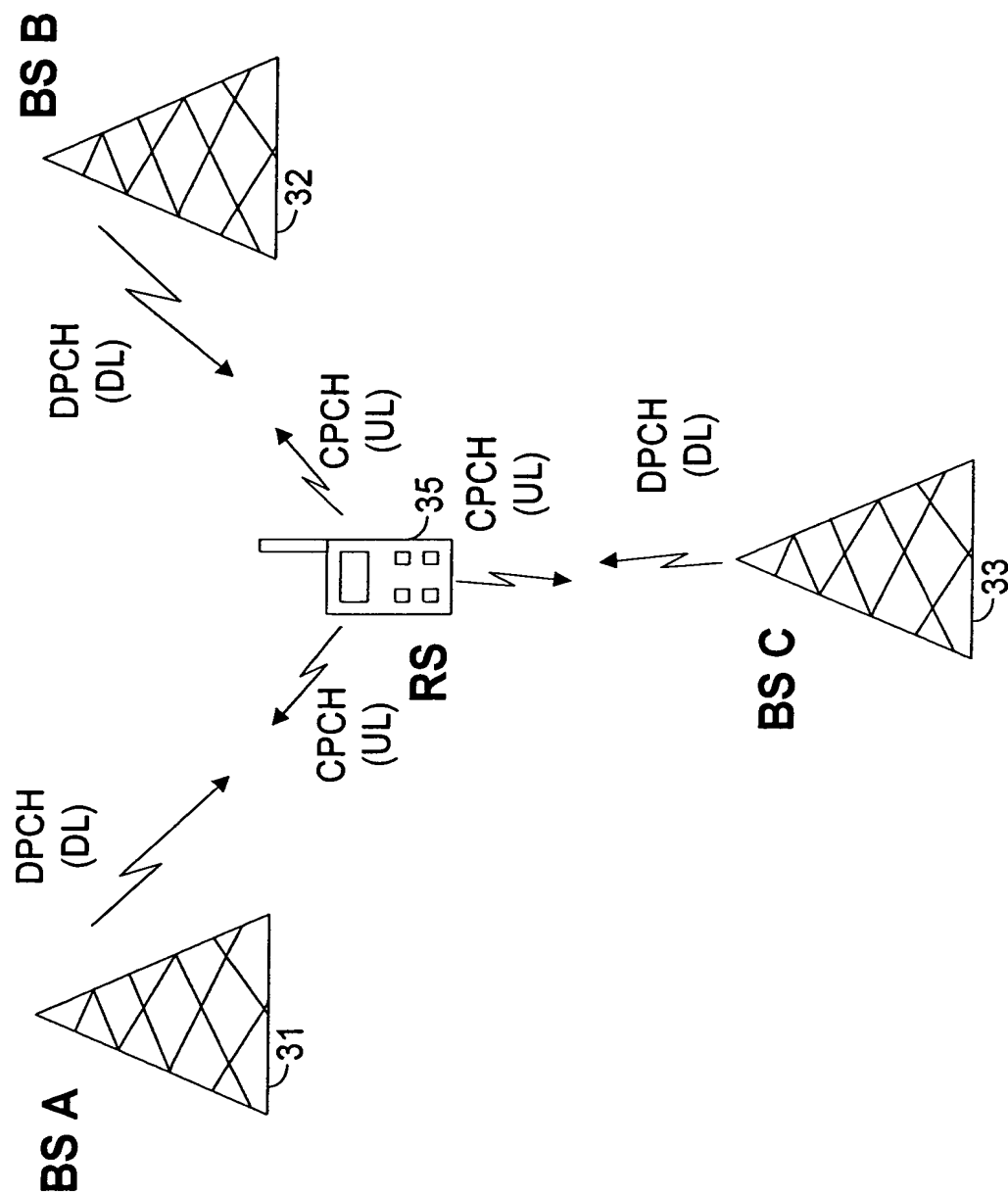
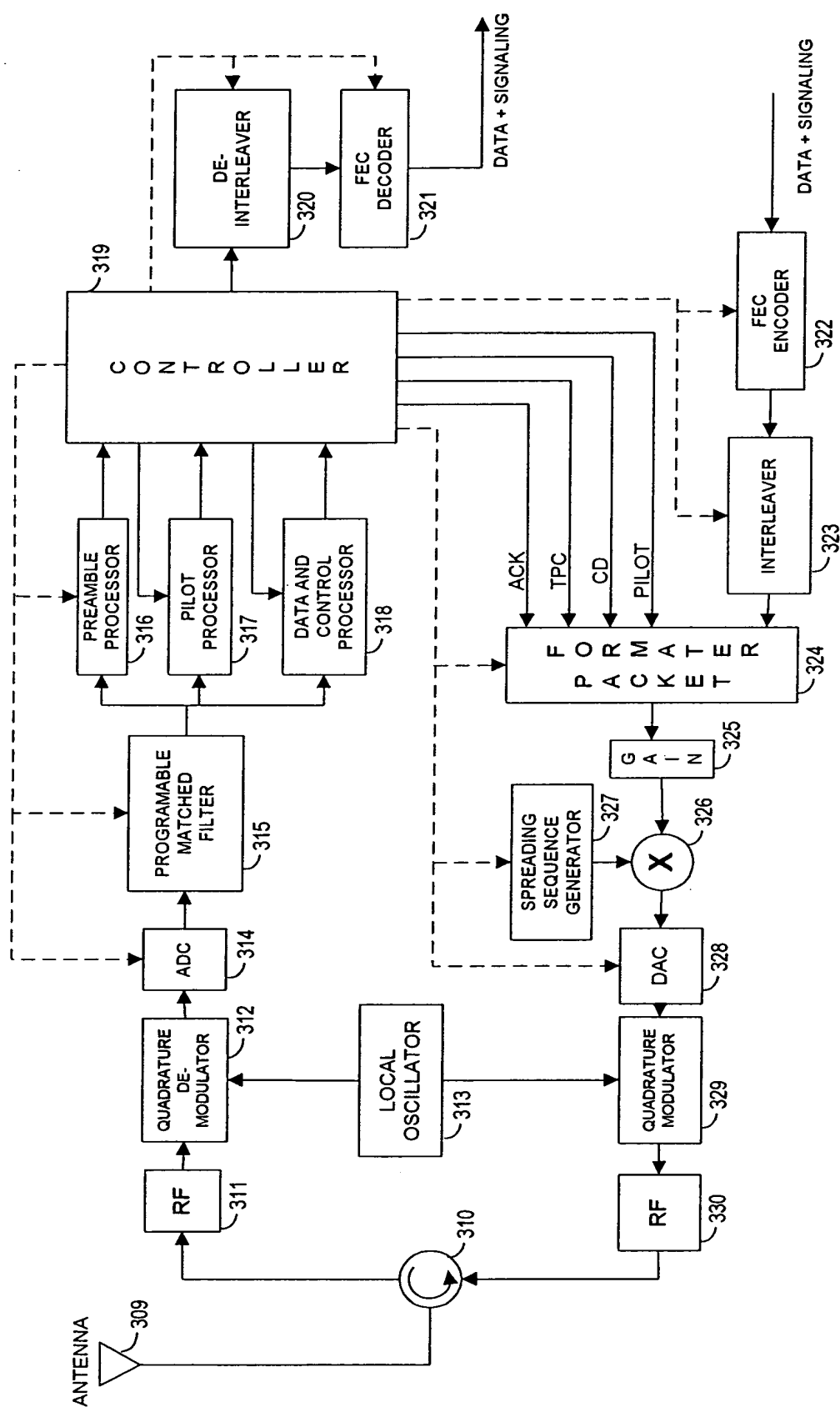


FIG. 2





### FIG. 3

## DATA

CONTROL - 1019

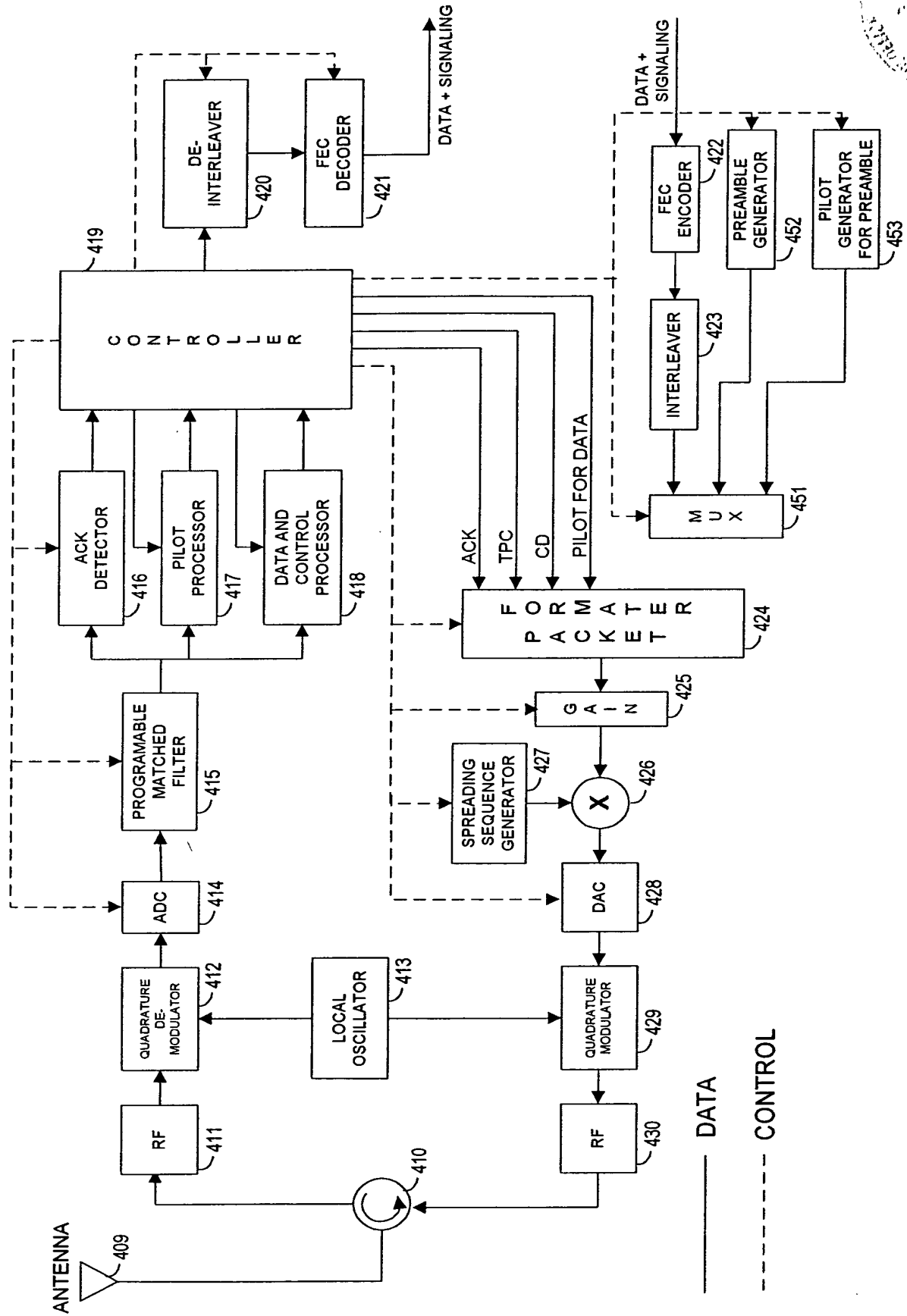


FIG. 4

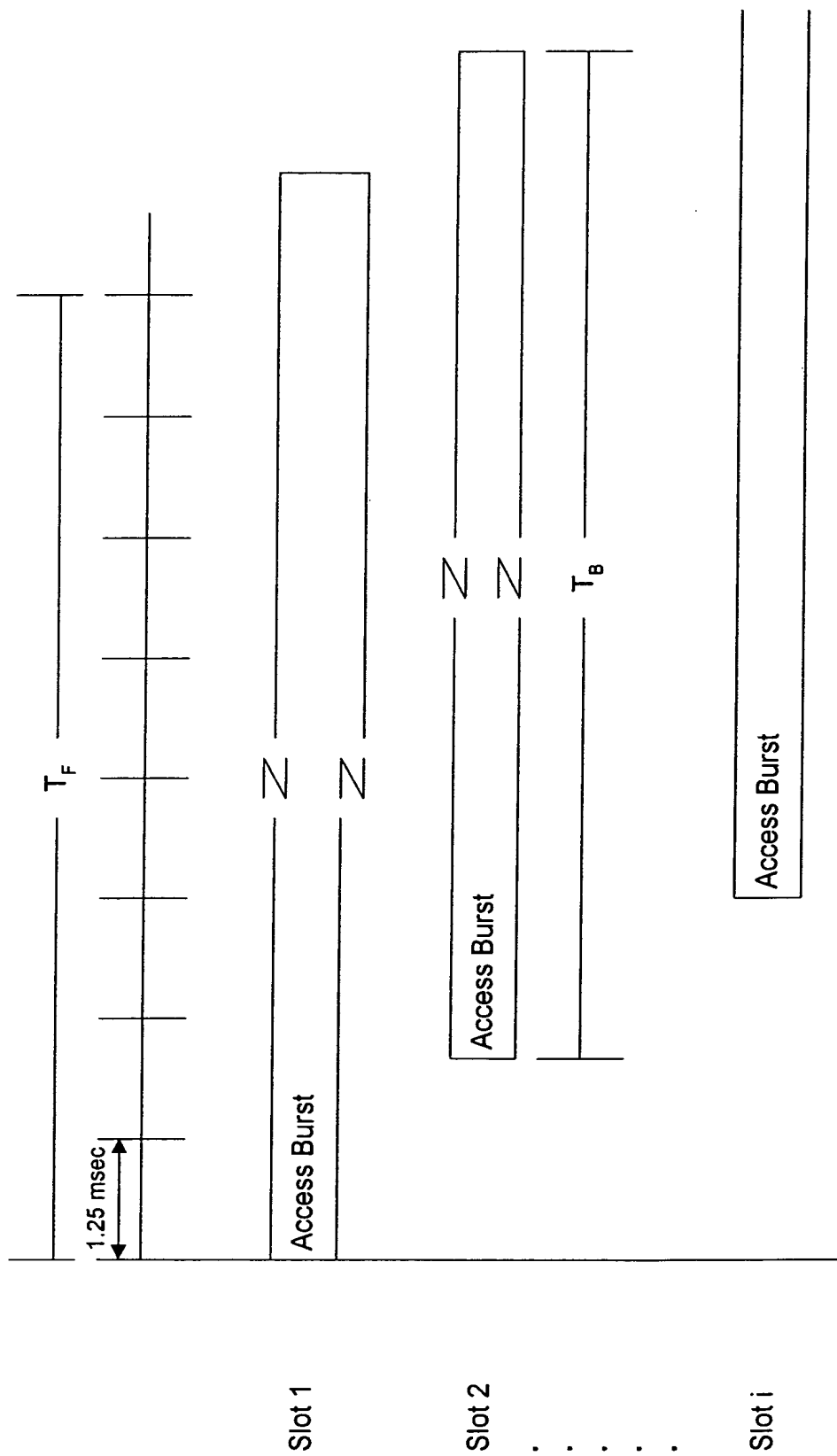


FIG. 5



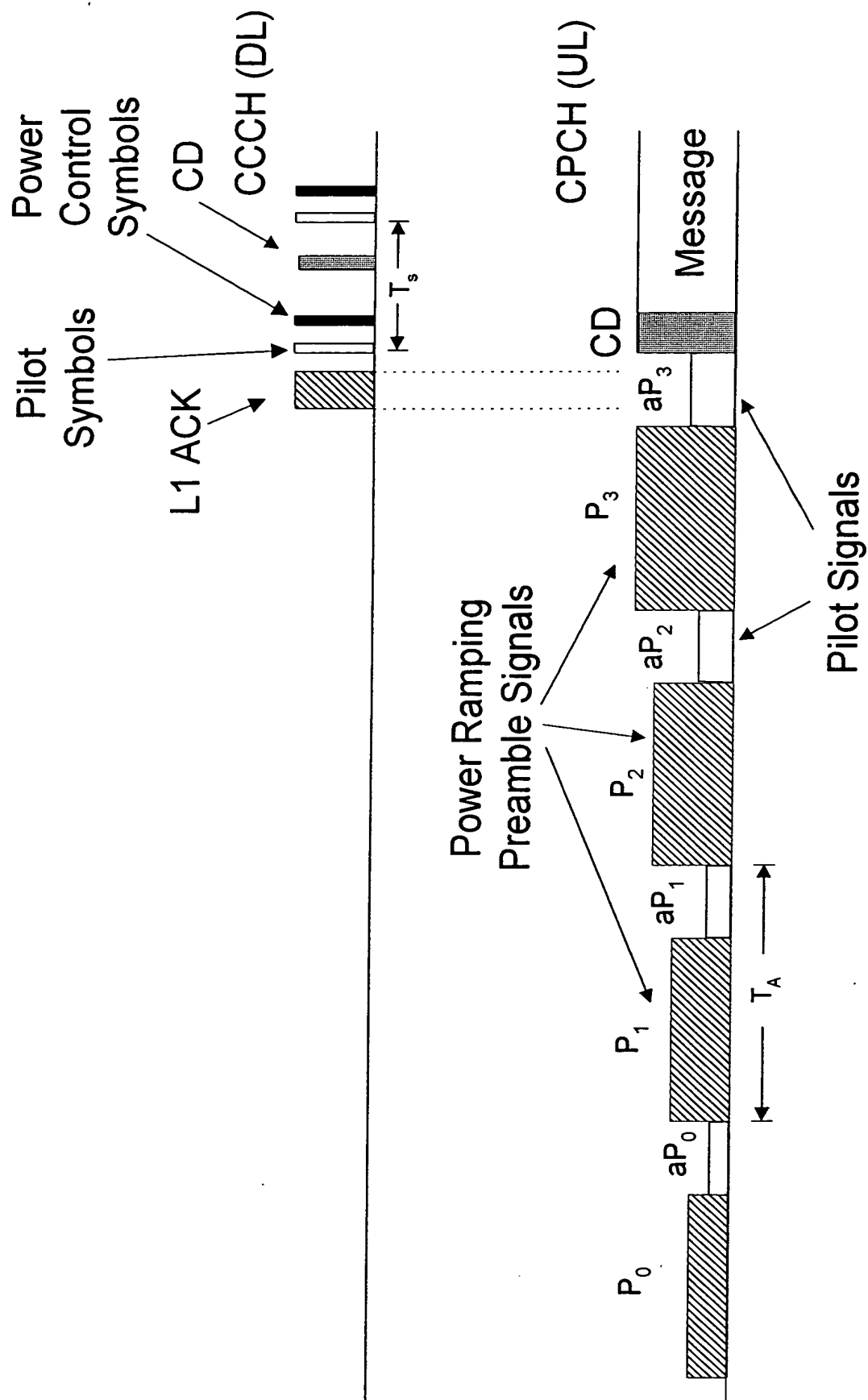
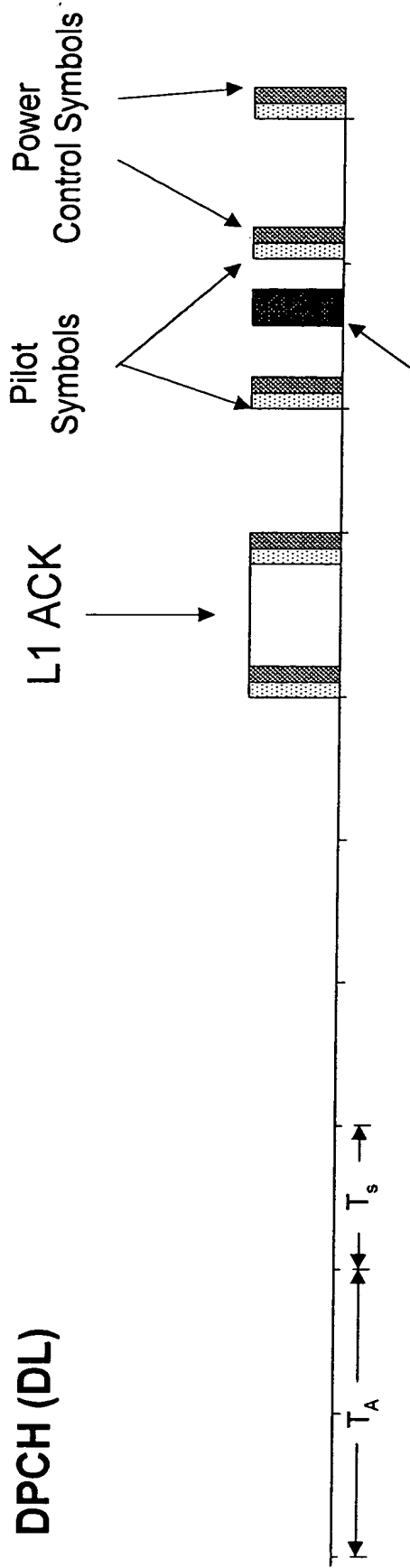


FIG. 6

# DPCH (DL)



# CPCH (UL)

Power Ramping  
Preambles

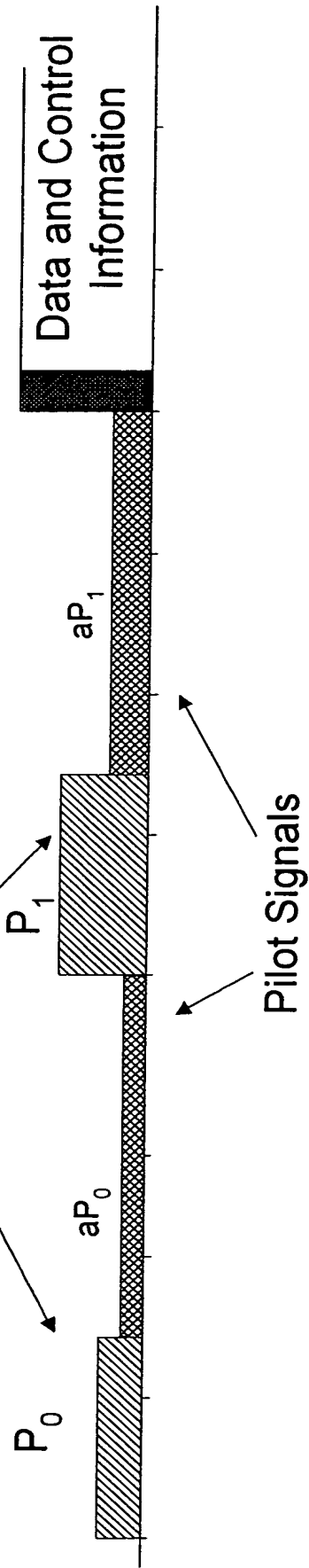


FIG. 7

$g_{k,0}A$	$g_{k,1}A$	$g_{k,2}A$	-----	$g_{k,N-1}A$
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(A)

$g_{k,0}A_{k,i0}$	$g_{k,1}A_{k,i1}$	$g_{k,2}A_{k,i2}$	-----	$g_{k,N-1}A_{k,i(N-1)}$
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(B)

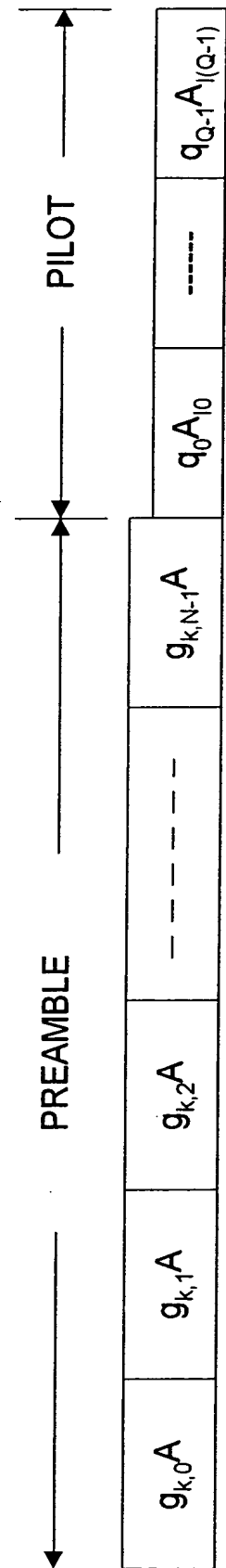
$$A_{k,ij} \in [A_0, A_1, A_2, \dots, A_{N-1}]$$

$$A_{k1,ij} \neq A_{k2,ij}$$

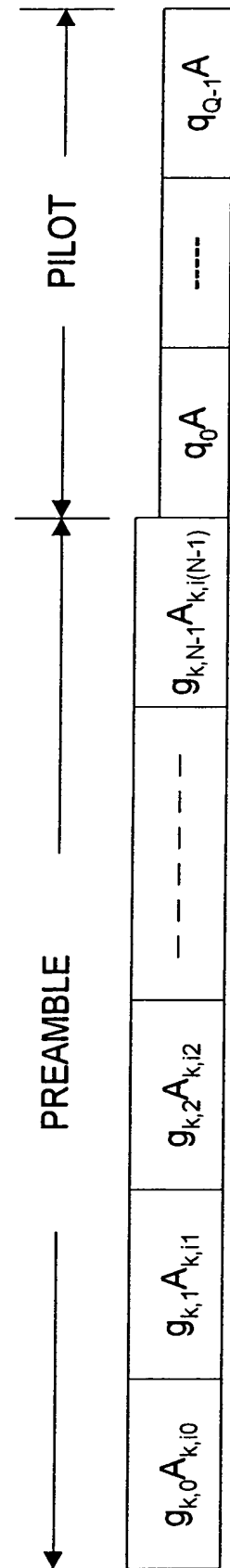
FIG. 8







(A)



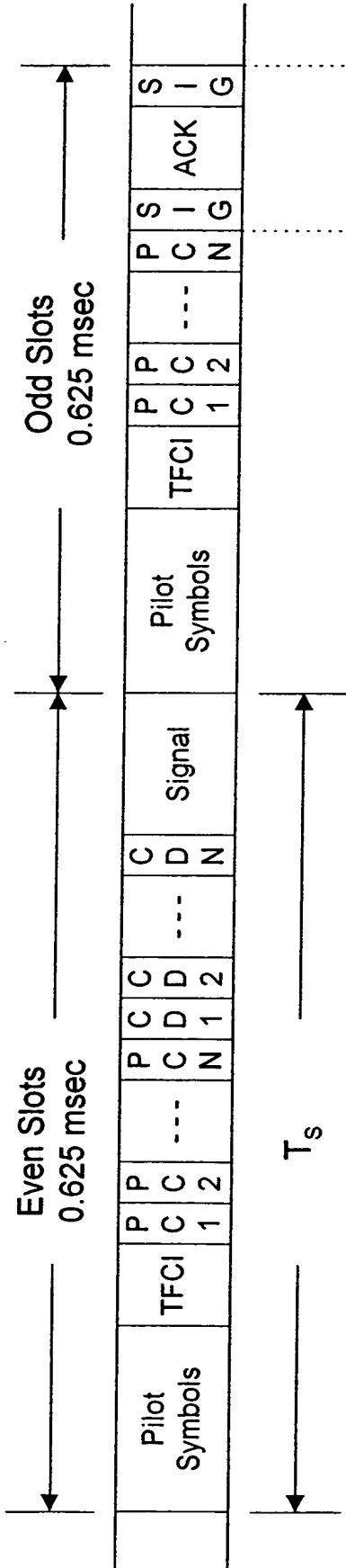
(B)

$$A_{k,ij} \in [A_0, A_1, A_2, \dots, A_{N-1}]$$

$$A_{k1,ij} \neq A_{k2,ij}$$

FIG. 9

# CCCH (DL)



# CPCH (UL)

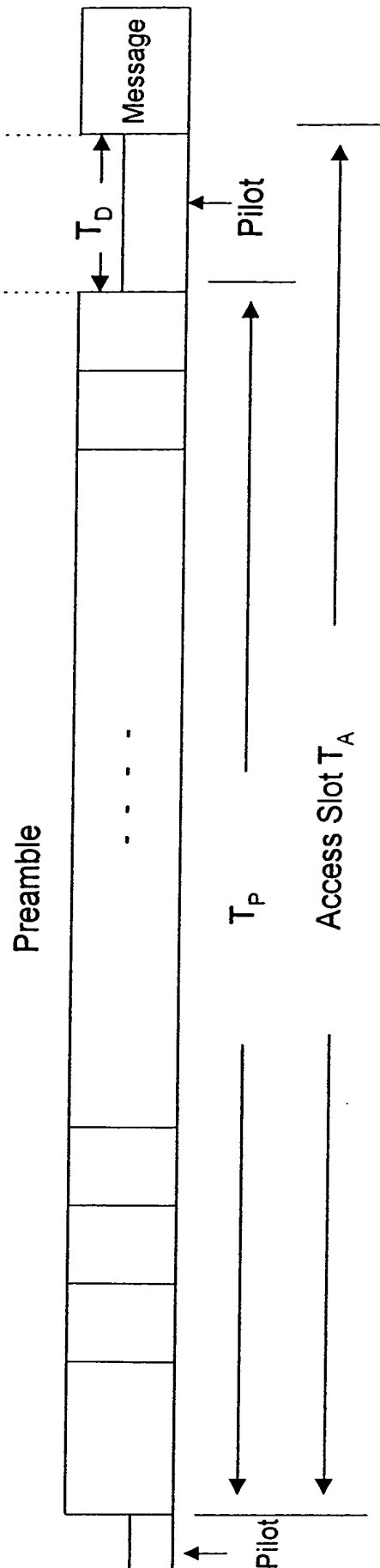


FIG. 10



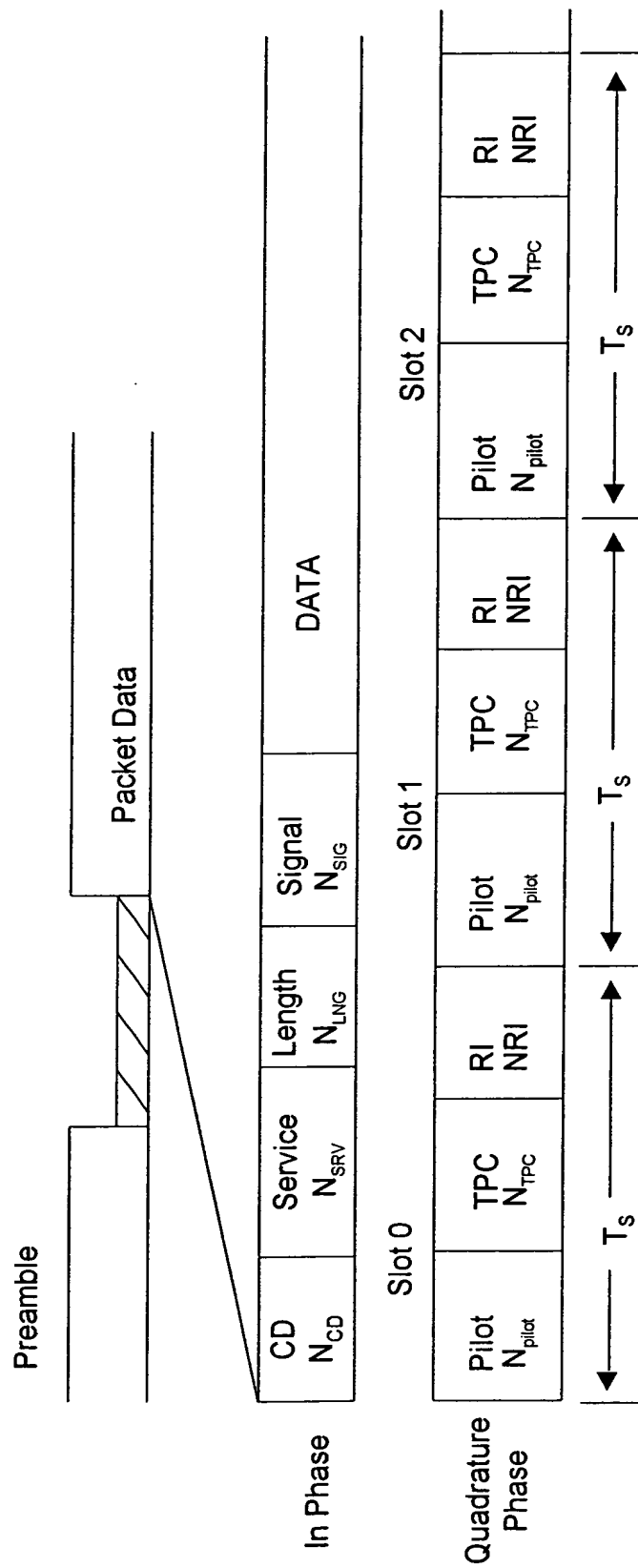


FIG. 11